

JANUARY NEWSLETTER



January 28-30, 2020 Santa Clara Convention Center, Santa Clara, CA



Join us at our **Booth 952** and experience a series of live demos at our partners' booths.

and semiconductor industries who are looking to engineer the technology of tomorrow.

As part of the ever-evolving data center environment, MultiLane aims to ensure that its technology keeps pace with industry needs.

We are excited to showcase our latest test solutions that enable cost effective validation and manufacturing processes for the 400G ecosystem.

- Learn more about our PAM4 Bit Error Rate Testers
- Check out our 400G Forward Error Correction (FEC) solutions
- Find out how our TDR cable tester can ensure your network is ready for 400G
- Explore our fully automated transceiver test solution

400G 4x56 GBd PAM4 BERTs







400G 8x28 GBd PAM4 BERTs





Host/margin tester

400G BERT 4x35 GHz TDR/TDT and DSO



Cable/TIA tester

400G-DR4 Manufacturing Testing



